

This Page Is Inserted by IFW Operations  
and is not a part of the Official Record

## **BEST AVAILABLE IMAGES**

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

**IMAGES ARE BEST AVAILABLE COPY.**

As rescanning documents *will not* correct images,  
please do not report the images to the  
Image Problem Mailbox.

**THIS PAGE BLANK (USPTO)**



⑪ Publication number : **0 615 285 A2**

⑫

## EUROPEAN PATENT APPLICATION

⑳ Application number : **94301777.2**

⑤① Int. Cl.<sup>5</sup> : **H01L 21/60**

㉔ Date of filing : **11.03.94**

③① Priority : **11.03.93 ZA 931752**

④③ Date of publication of application :  
**14.09.94 Bulletin 94/37**

⑧④ Designated Contracting States :  
**AT BE CH DE DK ES FR GB GR IE IT LI LU MC  
NL PT SE**

⑦① Applicant : **CSIR**  
**Scientia, Meiring Naude Street**  
**Pretoria Transvaal (ZA)**

⑦② Inventor : **Marsh, Michael J.C.**  
**234 Regent Street East,**  
**Observatory**  
**Johannesburg, Transvaal (ZA)**

Inventor : **Gouws, Gideon J.**  
**79 Carica Street,**  
**Kilner Park**

**Pretoria, Transvaal (ZA)**  
Inventor : **Carson, Mark H.**  
**597 Goukamma Road,**  
**Erasmuskloof Ext. 3**

**Pretoria, Transvaal (ZA)**  
Inventor : **Marais, Mario A.**  
**609 Vacy Lyle Street,**  
**Elardus Park**

**Pretoria, Transvaal (ZA)**  
Inventor : **Hodson, Trevor M.**  
**20 Sirkoon Street,**  
**Jukskei Park**  
**Randburg, Transvaal (ZA)**

⑦④ Representative : **Crawford, Fiona Merle**  
**Elkington and Fife**  
**Prospect House**  
**8 Pembroke Road**  
**Sevenoaks, Kent TN13 1XR (GB)**

⑤④ Attaching an electronic circuit to a substrate.

⑤⑦ Integrated circuits such as transponders (10) are attached to a substrate (28) such as cardboard packaging material by adhering an integrated circuit to the substrate, applying a patch antenna (18) to the substrate and the integrated circuit, and applying a sealing layer (40) over the integrated circuit and at least a portion of the antenna. In variations of the method, an indentation (34) may be formed in the substrate into which the integrated circuit is placed. The patch antenna may be screen printed onto the substrate and the integrated circuit, using conductive ink, or may be a metallic foil which is applied to the substrate and the integrated circuit. Where the integrated circuit is a transponder, it can be programmed with an identification code, either before or after being attached to the substrate.

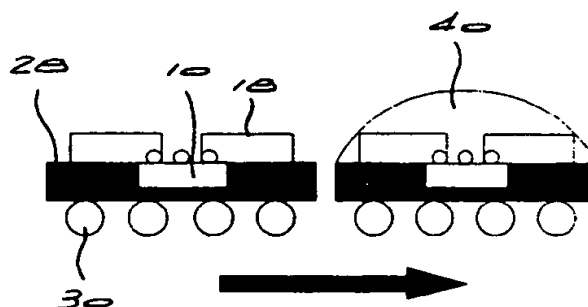


Figure 6

EP 0 615 285 A2

**BACKGROUND OF THE INVENTION**

South African patent application no. 92/0039 describes an identification system in which transponders are attached to articles to be identified, such as containers or packaged articles in a supermarket, with the transponders carrying an identification code which identifies each article, either uniquely or as one of a number of articles of a particular type.

In order for such a system to be economically viable, it is important to provide a relatively simple but effective way of attaching the transponders to the containers or packages.

**SUMMARY OF THE INVENTION**

According to the invention a method of attaching an electronic circuit to a substrate comprises:

providing an electronic circuit having at least two electrical terminals;

securing the electronic circuit to the substrate; and

applying a conductive element to the substrate and the electronic circuit to define an antenna, so that the conductive element makes electrical contact with the at least two electrical terminals.

The conductive element may comprise a layer of conductive ink, printed in the shape of the antenna.

The conductive ink may comprise carbon or metallic particles.

Alternatively, the conductive element may comprise a metallic foil.

The metallic foil may be applied to the electronic circuit prior to securing the integrated circuit to the substrate, with the foil and the circuit being applied together to the substrate.

Alternatively, the metallic foil may be applied to the substrate after the electronic circuit has been secured to the substrate.

The metallic foil may have an adhesive layer thereon for securing the foil to the substrate.

The adhesive layer is preferably removed from the metallic foil in areas corresponding to the position of the electrical terminals of the electronic circuit prior to application of the foil to the electronic circuit or the substrate.

The metallic foil may be applied to the substrate prior to securing the electronic circuit to the substrate, and the electronic circuit may be subsequently attached to predetermined areas of the metallic foil, thereby to secure the electronic circuit to the substrate.

The electrical contacts of the electronic circuit may be attached to predetermined areas of the foil using a conductive adhesive.

The method may include applying solder to the at least two electrical terminals, applying the conductive layer to the substrate so that it makes contact with the terminals, and reflowing the solder to join the antenna to the terminals.

The method may include forming a cavity or indentation in the substrate to receive the electronic circuit.

The cavity or indentation may be formed by punching or stamping the substrate.

Alternatively, the cavity or indentation may be formed by moulding.

In another version of the invention, the cavity or indentation is formed in a plastics substrate by the application of a heated tool.

In a further version of the invention the cavity or indentation is formed in a plastics substrate by heating the electronic circuit and pressing it into the substrate.

Preferably, an upper surface of the electronic circuit bearing the electrical terminals is substantially flush with the surface of the substrate after being secured thereto.

The method may include applying a protective coating over the electronic circuit and at least a portion of the antenna.

The protective coating may comprise a layer of settable sealant.

Alternatively, the protective coating may comprise a label.

The electronic circuit will in most cases be an integrated circuit such as a transponder.

In the case of a transponder which is programmable with an identity code, the method may include programming the identity code into the transponder before or after securing it to the substrate.

The method may include applying contacts of a programming device to both the transponder and the antenna after securing the transponder and the antenna to the substrate, to program the transponder.

The method may include applying at least one contact element to the substrate which is in contact with a respective terminal of the transponder, and applying contacts of a programming device to the antenna and the at least one contact element after securing the transponder and the antenna to the substrate, to program the transponder.

Preferably, the method includes applying a protective coating to the transponder, a portion of the antenna

and a portion of the contact element prior to programming of the transponder, the contacts of the programming device being applied to uncoated portions of the antenna and the contact element.

In one embodiment of the invention, the substrate comprises at least first and second components, the second component being secured to the first component after attachment of the electronic circuit and the antenna thereto, so that the electronic circuit and the antenna are substantially enclosed.

The invention extends to a packaging component comprising a substrate to which an integrated circuit has been attached by the above method.

The substrate may comprise cardboard, or may comprise glass, plastics or metal, for example.

The substrate may define a container, or a closure for a container, for example.

The invention further extends to an identification tag comprising a substrate to which an integrated circuit has been attached by the above method.

#### **BRIEF DESCRIPTION OF THE DRAWINGS**

- 15 **Figure 1** is a schematic plan view of an integrated circuit transponder with a patch antenna applied thereto;
- Figures 2 to 6** illustrate schematically a method of programming the transponder and attaching it to a substrate;
- Figure 7** is a flow chart summarising in the steps illustrated in Figures 2 to 6;
- 20 **Figure 8** is a second flow chart, relating to a second method according to the invention;
- Figures 9 to 11** illustrate schematically the second method of attaching the transponder to the substrate;
- Figure 12** is a third flow chart, relating to a third version of the method of the invention;
- Figures 13 to 17** illustrate schematically the third method of attaching the transponder to the substrate;
- 25 **Figure 18** is a fourth flow chart, relating to a fourth version of the method according to the invention;
- Figures 19 to 23** illustrate schematically the fourth method of attaching the transponder to the substrate;
- Figure 24** is a fifth flow chart, relating to a fifth version of the method of the invention;
- 30 **Figures 25 to 28** illustrate schematically the fifth method of attaching the transponder to the substrate;
- Figure 29** is a partial sectional side view of a cap for a container to which a transponder has been attached by the method of the invention; and
- Figure 30** is a plan view of a typical patch antenna employed in the method of the invention.

#### **DESCRIPTION OF EMBODIMENTS**

Figure 1 is a schematic plan view of an integrated circuit transponder 10 which is formed with three conductive contacts or terminals 12, 14 and 16 on its upper surface, and which is embedded in the surface of a substrate (see below). Applied to the substrate over the transponder 10 is a rectangular patch antenna 18 which has an elongate central slot 20. The patch antenna 18 is oriented so that areas thereof on either side of the slot 20 are in electrical contact with the terminals 12 and 16 of the transponder, while the central terminal 14 of the transponder is in the slot 20, clear of the patch antenna.

The transponder and the patch antenna are designed to operate at relatively high frequencies, in the region of 915 MHz, and the patch antenna is sized accordingly. A typical antenna is about 50 mm by 25 mm in size. The exact shape, size and type of the antenna will depend in practice on the required radiation pattern and the operating frequency.

The substrate (not shown in Figure 1) is typically the surface of a package or container which is required to be identified by the transponder. Thus, the transponder is programmed with an identification code, which can identify the article to which it is attached, either uniquely or as one of a number of identical articles. Particularly in the latter case, where relatively low-cost articles such as packages of foodstuffs are required to be identified in a supermarket or other retail outlet, the cost of providing the coded transponder and securing it to the package becomes critical.

Referring now to Figures 2 to 6, the transponder 10 is illustrated schematically on a conveyor belt 22. A programming unit 24 stores the identification code which is to be loaded into the transponder, and is connected to the terminals 12, 14 and 16 of the transponder as it passes the programming unit by means of spring loaded contact pins 26. The transponder 10 contains an EPROM, and the desired identification code (typically a 38 to 64 bit code) is burnt into the EPROM. The transponder 10 then moves on, and a following transponder is programmed.

In Figure 3, sections of cardboard sheet 28 are shown being moved on rollers 30. The cardboard sheets are blanks which will be folded into boxes, for example, for containing foodstuffs. A punch 32 is operated as each cardboard blank passes underneath it, creating a cavity or indentation 34 at a desired location in each blank.

As shown in Figure 4, a layer of adhesive 36 is now applied to the underside and the sides of the transponder 10. Using a pick and place machine 38 (see Figure 5) the transponder 10 is located in the indentation or cavity 34 in the blank 28 as it passes the pick and place machine, so that the transponder is secured to the cardboard blank with its upper surface flush with the upper surface of the blank.

Apart from securing the transponder 10 to the cardboard substrate 28, the adhesive 36 provides environmental protection for the integrated circuit, for example, against moisture. In addition, the adhesive serves to insulate the substrate of the circuit, that is, the sides and the bottom of the circuit, which are normally either at the ground or supply potential of the integrated circuit. This is important to prevent the integrated circuit substrate being shorted to any portion of the antenna which is applied to the substrate in the next step.

In the next step, the antenna 18 is applied to the surface of the substrate 28, by printing a conducting ink on the substrate in the required shape. The ink can be metallic or carbon based, for example, such as "Colag" ink or graphite paste. Finally, a protective coating 40 is applied to the substrate over the transponder 10 and the antenna 18, to protect it from moisture and mechanical damage. The protective coating can be another layer of ink, which is preferably non-conducting, or else may comprise a plastic sealant. As a further alternative, the protective layer may comprise a label, such as a self-adhesive label, upon which is printed a conventional bar code with a number corresponding to the identification code programmed into the transponder 10. This allows the final package to be identified both by means of the transponder 10 and the bar code.

The above described steps are set out in the simplified flow chart of Figure 7.

The cardboard blank 28 is now fed through the remaining stages of production, including a printing stage, and a cutting and folding stage in which the blank is formed into a cardboard box which can be filled as usual. The completed and filled box can be delivered to a retailer in the usual way, where it can be identified in a conventional manner, or by using the identification system described in South African patent application no. 92/0039, the contents of which are incorporated herein by reference.

In an alternative version of the invention, the antenna 18 is not printed on the substrate using conductive ink, but comprises a conductive foil layer which is applied to the surface of the substrate. This version of the method is set out in the simplified flow chart of Figure 8, and is illustrated in Figures 9 to 11. The antenna 18 comprises a sheet of self-adhesive metallic foil 42, which is stamped or pre-cut from a strip of the foil. As shown in Figure 9, the integrated circuit transponder 10 is connected to the underside of the foil strip 42, in an area where the conventional adhesive layer 44 on the foil strip has been removed, using a conductive adhesive compound.

One example of such a conductive/adhesive material is an electrically conductive silver filled epoxy, known commercially as EPO-TEK H20E-PFC. This is a two part epoxy, which can be cured in 15 minutes at 120°C, and which has a sheet resistance of 0.0001 to 0.0004 ohm-cm.

Ideally, a number of transponders 10 are attached to respective foil antennas and are provided on a continuous roll for application to cardboard blanks in a printing process. A layer of adhesive 46 is optionally applied to the underside of the transponder 10. A protective coating 48 of a suitable sealant is applied to the upper surface of the antenna, sealing the area where the antenna and the transponder are connected.

As shown in Figure 10, the transponder/antenna sub-assembly is placed in abutment with the cardboard substrate 28, so that the transponder 10 is located in the punched cavity or indentation 34. A roller is passed over the transponder/antenna sub-assembly to secure it to the cardboard blank, by means of the adhesive 44 on the foil 42 of the antenna, and preferably also by means of the layer of adhesive 46 applied to the underside of the transponder 10, with the completed assembly shown in Figure 11.

In a hybrid version of the above two methods, a self-adhesive foil antenna patch can be applied to the substrate 28 after the transponder 10 has been secured thereto, instead of printing the antenna over the transponder.

A third method according to the invention is illustrated in Figures 12 to 17. In this version of the invention, the transponder 10 is not programmed prior to being secured to a substrate, as in the above described two methods. Figure 12 indicates the steps of this method in flow chart form. As shown in Figure 13, in the first step, a conductive antenna 50 is printed onto or otherwise secured to a substrate 52. The antenna 50 comprises first and second halves 54 and 56. In addition, a central contact strip 58 is printed between the two antenna halves. This strip is used in programming the transponder 10 with its identification code.

In Figure 14, conductive bumps 60 comprising a mixture of conductive and adhesive material are applied to contact areas on the antenna halves 54 and 56 and on the contact strip 58 by an applicator 62, for connection to contact points on the transponder itself.

Next, as indicated in Figure 15, a pick and place machine 64 is used to place the transponder chip 10 in a "flipped" or face down orientation on the antenna 50, with respective contact points 12, 14 and 16 in contact with the respective contact bumps 60 on the antenna half 54, the contact strip 58 and the antenna half 56. A curing step follows, when the adhesive/conductive material connecting the transponder 10 to the antenna and the contact strip is cured and hardened. As shown in Figure 16, a protective coating 66 of a suitable sealant is then applied over the transponder and the antenna, in the area where the two make contact. However, a substantial portion of the entire antenna and at least a portion of the contact strip 58 remain uncovered (see the schematic plan view of Figure 16a) as they are still required for programming of the transponder.

Finally, as shown in Figure 17, a programming unit 24 having spring loaded contact pins 68 is positioned over the transponder 10 with its respective pins 68 in contact with the contact strip 58 and the antenna halves 54 and 56, and the transponder is then programmed with its identification code. If required, a further thin protective coating can be applied to cover the exposed portions of the antenna and the contact strip.

Of course, instead of a single contact strip, two or more such strips can be provided for purposes of programming the transponder.

A further embodiment of the invention is illustrated in Figures 18 to 23. Figure 18 summarises this version of the method in flow chart form. In this version of the method, an antenna comprising a first half 70 and a second half 72 is printed or applied to the surface of a substrate 74 as described above. A punch 76 with a curved end surface 78 is used to form an indentation 80 in the surface of the substrate in the central portion of the antenna. The central portion of the indentation 80, where a transponder 10 is to be attached, is flat. The curved face of the punch and the depth of the indentation 80 are chosen so that the antenna is not damaged during this step.

As shown in Figure 20, an applicator device 82 is used to form adhesive contact bumps 84 at the inner edges of the antenna halves 70 and 72, which correspond to contact points on the transponder chip. As shown in Figure 21, a pick and place machine 86 is then used to locate the transponder 10 on the antenna with its contact points in contact with the respective conductive bumps 84, similarly to the procedure described with reference to Figure 15. The adhesive/conductive material of the contact bumps 84 is then heat cured. As shown in Figure 22, the transponder 10 can now be programmed with its identification code by means of the above described programming unit 24 with its spring-loaded contact pins 68, which make contact with the antenna halves 70 and 72, and with the conductive substrate of the transponder chip 10.

Finally, a protective layer 88 of a suitable sealant is applied over the indentation in the centre of the antenna, over the transponder 10, leaving the substrate 74 with a substantially flat upper surface.

Still another version of the invention is illustrated in Figures 24 to 28. Figure 24 summarises this version of the method in flow chart form. In this embodiment, the substrate to which the transponder is attached is a housing having at least two components between which the transponder chip is held. The completed housing may be, for example, an identification or price tag which is applied to an article of merchandise, or an identification card.

As shown in Figure 25, a first half 90 of the housing is formed with an indentation or cavity 92 sized to receive a transponder chip. The housing half 90 also has a hole or other attachment means 94 formed in it, for later attachment of the housing to another article.

As shown in Figure 26, a pick and place machine 96 is used to place a transponder chip 10, the sides and bottom of which have been coated with an adhesive layer 98, into the cavity 92.

As shown in Figure 27, an antenna comprising first and second halves 100 and 102 is then printed or otherwise located over the transponder chip 10, in much the same manner as the first two embodiments of the invention described above. The second housing half 104 is then placed against the first housing half 90 and fixed to it, for example, by means of adhesive, a heat welding step, or another method. The second housing half 104 has an aperture 106 in it which is aligned with the aperture 94 in the first housing half, so that the resulting composite device has an aperture in it, allowing the device to be attached to another article as required.

The above described methods can be applied to a number of different substrates. Where an indentation must be made in the substrate, substrates of softer material such as cardboard and certain plastics materials are preferred. In some cases, suitable indentations or cavities can be moulded into plastics or other substrates, and it is also possible to punch or cut cavities or indentations in harder materials such as a metals or glass, depending on requirements. Obviously, the type of operation which is required to create a cavity, where required, will affect the cost of the method.

In the case of a cardboard substrate, the transponder and its antenna can be attached prior to the folding of the cardboard into a box or other package. The cavity or indentation for receiving the transponder can easily be pushed into the cardboard prior to attachment of the transponder and subsequent folding of the cardboard, while the antenna can be printed or applied to the cardboard either before or after folding thereof.

In the case of a container such as a bottle, access to the interior of the container is restricted after man-

ufacture thereof, making it difficult to apply a transponder and an antenna to an inner surface of the container. Various solutions are possible. A plastics container will commonly be manufactured in a mould. During the moulding process, an indentation or cavity in the surface of the container can be included, into which the transponder chip can subsequently be placed and adhered. An antenna can then be printed or placed over the transponder chip, and a label placed over both the antenna and the transponder, concealing them. As an alternative, an indentation can be made in the plastics substrate using a heated tool.

As a further alternative, the transponder chip itself can be heated and pressed into the plastics substrate, so that it melts the adjacent plastic and adheres to it once it cools. The antenna can then be attached as described above.

In the case of a glass substrate, such as a bottle or other container, the transponder and its antenna will normally have to be attached after the manufacture of the container. This can be done in the same way as with a plastics substrate, by pre-indenting the glass during manufacture of the container, and by then hiding the transponder and its antenna under a label or a suitable tough protective coating. As an alternative, the transponder and its antenna can be incorporated in the lid of the container, as shown in Figure 29.

In Figure 29, a plastics screw cap 108 is shown, with the transponder chip 10 and its associated antenna 110 located on the inner end surface 112 of the cap, with a protective coating 114 of a suitable sealant. This is particularly convenient in the case of moulded plastics caps or lids. In such a case, at least a portion of the cap or lid, in the region where the transponder and its antenna are applied, must be non-metallic.

In the case of a metal substrate, it is not possible to print a conductive antenna directly onto the metal, as this would short out the antenna. Thus, it is necessary first to provide an insulating layer on the metal substrate before such a printing step. Alternatively, of course, a conductive foil antenna can be used with a suitable insulating layer between the antenna and the metal substrate.

The transponder 10 can be programmed with its identification code at the time of manufacturing. However, users of the transponders are afforded maximum flexibility if the identification code is programmed in at the time of attaching the transponder to its substrate. A number of possibilities arise. Firstly, the transponder can be programmed prior to being attached to the substrate or the antenna itself, for example by means of direct contact onto the contact points on the transponder chip. Secondly, the transponder can be programmed after attachment to the substrate but prior to the printing or application of the antenna onto the substrate. Thirdly, programming can be carried out after the attachment of both the transponder and printing or attachment of the antenna, but prior to the application of the protective coating. In this case, three contact points are normally required, one each to the two halves of the antenna and a third to a contact strip applied to the transponder, or directly onto a third contact on the transponder, or even directly to the substrate of the transponder. Fourthly, programming can even be carried out after the transponder and the adjacent portion of the antenna have been covered with a protective coating, by using the method described with reference to Figures 12 to 17.

In some applications it will be advantageous to program the transponder as late as possible in the process, after attachment of the antenna, and possibly also the protective coating. For example, where the substrate is an identification label to be applied to an article, with the transponder and its antenna attached thereto, the labels can be programmed shortly before they are issued for attachment to the relevant packaging.

In the process of attaching the transponder chip and the antenna to the substrate, a number of different adhesives and sealants may be required. As mentioned above, curing may be required in some of the steps. In order to increase manufacturing volumes, the various adhesives and sealants need to be rapid-curing types. Various different curing methods can be used, according to the type of substrate and the type of adhesive/sealant in use. For example, conventional heat sources such as ovens can be used for curing, or hot air blowers can be used. Infrared, ultra-violet or radio-frequency radiation can also be used, depending on the type of sealant. In some cases, cooling of the substrate may be appropriate for curing purposes.

In order to minimise the cost of the antenna, it is preferred to print it on the substrate using a conductive ink. In this context, the word "ink" is intended to mean a liquid or paste, for example, which can be applied in a desired shape or pattern to a substrate, and not necessarily a coloured or visible liquid. The ink contains a conductive component, such as particles of metal or carbon. A number of different printing methods can be used, such as silk screen methods, stencilling, spraying or conventional lithography. As mentioned above, the exact shape and size of the antenna depends on the frequency used and the radiation pattern required, which in turn depends on the specific application. A typical antenna design is illustrated in Figure 30, and is seen to comprise two halves 116 and 118, each of which has a base region 120 from which extend a plurality of parallel fingers 122. The spacing  $d$  between the halves 116 and 118 is determined by the spacing of the relevant contacts or terminals on the integrated circuit.

The materials used for the conductive ink must have a sufficiently low resistance to allow sufficient current flow in the antenna to allow the transponder to be powered up at the required operating range. In some cases, the printed ink may need to be flexible, to allow bending or twisting of the antenna. Table 1 shows the results



obtained with silver epoxy and silver ink, which were found to be suitable in prototype applications of the invention. Another possibility is ink containing carbon particles.

TABLE 1

TYPE OF INK TESTED		
TEST PERFORMED	SILVER EPOXY	SILVER INK
Sheet Resistance (mOhm/Square)	35.5	30.5
Contact Resistance - Silver Epoxy contact (mOhm)	10	1000
Bending and Torsion	Passed	Passed
Antenna Gain (dB)	0.5	-2

The contacts between the transponder chip and the antenna are important and a number of alternative methods of creating acceptable contacts are possible. As described above, the use of an adhesive material such as an epoxy resin with a conductive filler can be used to obtain an electrically adequate and physically strong join between the transponder and the antenna. An alternative is to provide solder bumps on the contact points of the transponder chip, which can be created by passing the wafer containing the chips through a wave soldering machine, or by screen printing solder paste onto the contact points, for example. Once the antenna is printed or applied to these solder bumps, the solder can be reflowed by heating, effectively providing soldered joints between the transponder and the antenna.

Although the above described methods have been described with reference to the printing or attachment of a patch antenna to the substrate and the integrated circuit transponder, this is merely exemplary, and is appropriate for the frequency range employed in the prototype system. Obviously, depending on the application of the end product and the specifications of the system in which it is to be used, different types of antenna may be employed. For example, a loop or coil of fine wire could be attached to each antenna terminal of the integrated circuit. Alternatively, elongate antenna elements of wire or foil could be connected to each antenna terminal. Of course, such loops, coils or elongate antenna elements could be printed in much the same way as described above. Which method of forming and applying the antenna is chosen will depend on the volumes required. In high volume applications, the printing of the antenna using a conductive ink or the like is advantageous.

The above described methods make it possible to attach integrated circuit transponders (or other electronic circuits) to suitable substrates in large volumes, allowing the implementation of an automatic identification system which can be applied to large numbers of articles of a relatively low cost.

Although different aspects of the method of the present invention have been described with reference to the attachment of integrated circuit transponders to various substrates, it will be appreciated that the invention is not limited to use with transponders, but that other electronic circuits (not necessarily integrated circuits) can be attached to substrates in the same way.

### Claims

1. A method of attaching an electronic circuit to a substrate characterised in that it comprises:
  - providing an electronic circuit having at least two electrical terminals;
  - securing the electronic circuit to the substrate; and
  - applying a conductive element to the substrate and the electronic circuit to define an antenna, so that the conductive element makes electrical contact with the at least two electrical terminals.
2. A method according to claim 1 characterised in that the conductive element comprises a layer of conductive ink, printed in the shape of the antenna.
3. A method according to claim 2 characterised in that the conductive ink comprises carbon or metallic particles.
4. A method according to claim 1 characterised in that the conductive element comprises a metallic foil.

5. A method according to claim 4 characterised in that the metallic foil is applied to the electronic circuit prior to securing the integrated circuit to the substrate, with the foil and the circuit being applied together to the substrate.
- 5 6. A method according to claim 4 characterised in that the metallic foil is applied to the substrate after the electronic circuit has been secured to the substrate.
7. A method according to any one of claims 4 to 6 characterised in that the metallic foil has an adhesive layer thereon for securing the foil to the substrate.
- 10 8. A method according to claim 7 characterised in that the adhesive layer is removed from the metallic foil in areas corresponding to the position of the electrical terminals of the electronic circuit prior to application of the foil to the electronic circuit or the substrate.
- 15 9. A method according to claim 4 characterised in that the metallic foil is applied to the substrate prior to securing the electronic circuit to the substrate, and the electronic circuit is subsequently attached to predetermined areas of the metallic foil, thereby to secure the electronic circuit to the substrate.
10. A method according to any one of claims 4 to 9 characterised in that includes attaching the electrical contacts of the electronic circuit to predetermined areas of the foil using a conductive adhesive.
- 20 11. A method according to any one of claims 1 to 10 characterised in that it includes applying solder to the at least two electrical terminals, applying the conductive layer to the substrate so that it makes contact with the terminals, and reflowing the solder to join the antenna to the terminals.
- 25 12. A method according to any one of claims 1 to 11 characterised in that it includes forming a cavity or indentation in the substrate to receive the electronic circuit.
13. A method according to claim 12 characterised in that the cavity or indentation is formed by punching or stamping the substrate.
- 30 14. A method according to claim 12 characterised in that the cavity or indentation is formed by moulding.
15. A method according to claim 12 characterised in that the cavity or indentation is formed in a plastics substrate by the application of a heated tool.
- 35 16. A method according to claim 12 characterised in that the cavity or indentation is formed in a plastics substrate by heating the electronic circuit and pressing it into the substrate.
17. A method according to any one of claims 12 to 16 characterised in that an upper surface of the electronic circuit bearing the electrical terminals is substantially flush with the surface of the substrate after being secured thereto.
- 40 18. A method according to any one of claims 1 to 17 characterised in that it includes applying a protective coating over the electronic circuit and at least a portion of the antenna.
- 45 19. A method according to claim 18 characterised in that the protective coating comprises a layer of settable sealant.
20. A method according to claim 18 characterised in that the protective coating comprises a label.
- 50 21. A method according to any one of claims 1 to 20 characterised in that the electronic circuit is an integrated circuit.
22. A method according to claim 21 characterised in that the electronic circuit is a transponder which is programmable with an identity code, the method including programming the identity code into the transponder before or after securing it to the substrate.
- 55 23. A method according to claim 22 characterised in that it includes applying contacts of a programming device to both the transponder and the antenna after securing the transponder and the antenna to the substrate, to program the transponder.

24. A method according to claim 22 characterised in that it includes applying at least one contact element to the substrate which is in contact with a respective terminal of the transponder, and applying contacts of a programming device to the antenna and the at least one contact element after securing the transponder and the antenna to the substrate, to program the transponder.

25. A method according to claim 24 characterised in that it includes applying a protective coating to the transponder, a portion of the antenna and a portion of the contact element prior to programming of the transponder, the contacts of the programming device being applied to uncoated portions of the antenna and the contact element.

26. A method according to any one of claims 1 to 25 characterised in that the substrate comprises at least first and second components, the second component being secured to the first component after attachment of the electronic circuit and the antenna thereto, so that the electronic circuit and the antenna are substantially enclosed.

27. A packaging component characterised in that it comprises a substrate to which an integrated circuit has been attached by the method of any one of claims 1 to 26.

28. A packaging component according to claim 27 characterised in that the substrate comprises cardboard.

29. A packaging component according to claim 27 characterised in that the substrate comprises glass, plastics or metal.

30. A packaging component according to any one of claims 27 to 29 characterised in that the substrate defines a container.

31. A packaging component according to any one of claims 27 to 29 characterised in that the substrate defines a closure for a container.

32. An identification tag characterised in that it comprises a substrate to which an integrated circuit has been attached by the method of any one of claims 1 to 26.

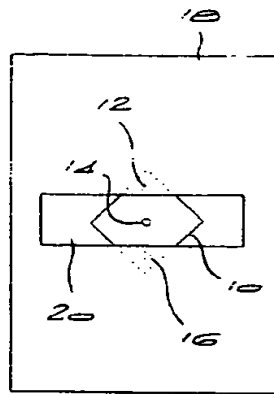


Figure 1

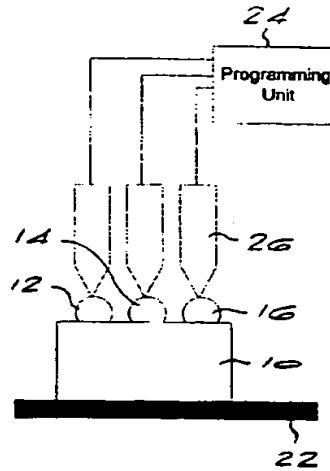


Figure 2

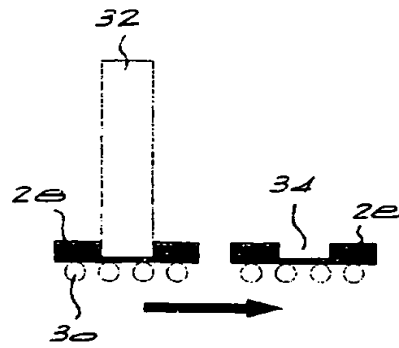


Figure 3

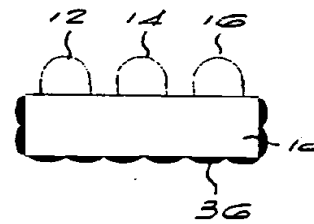


Figure 4

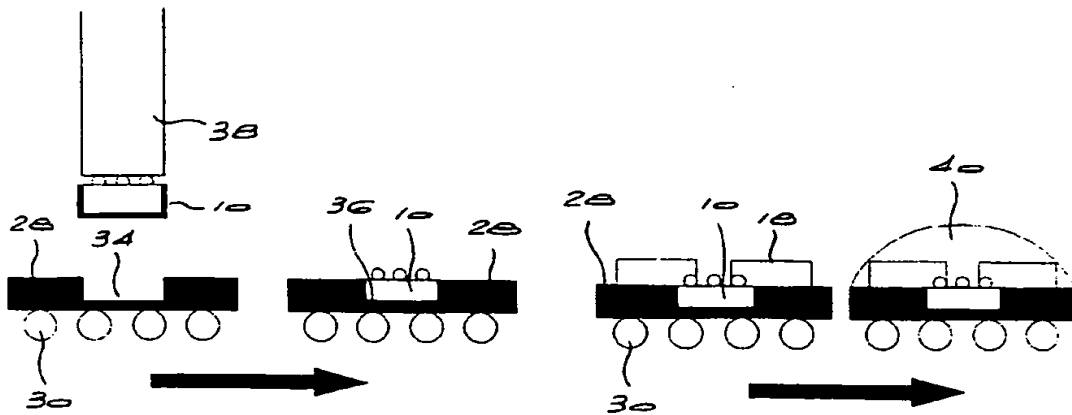


Figure 5

Figure 6

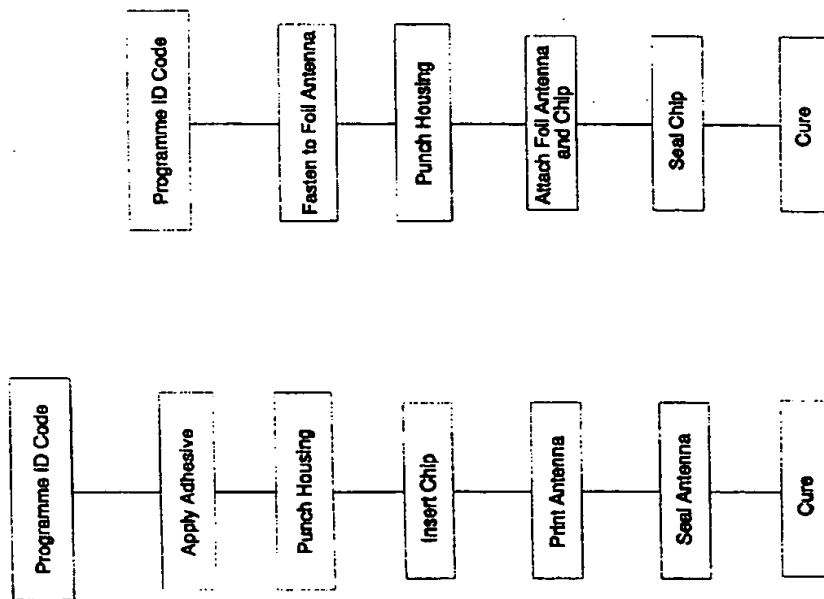


Figure 8

Figure 7

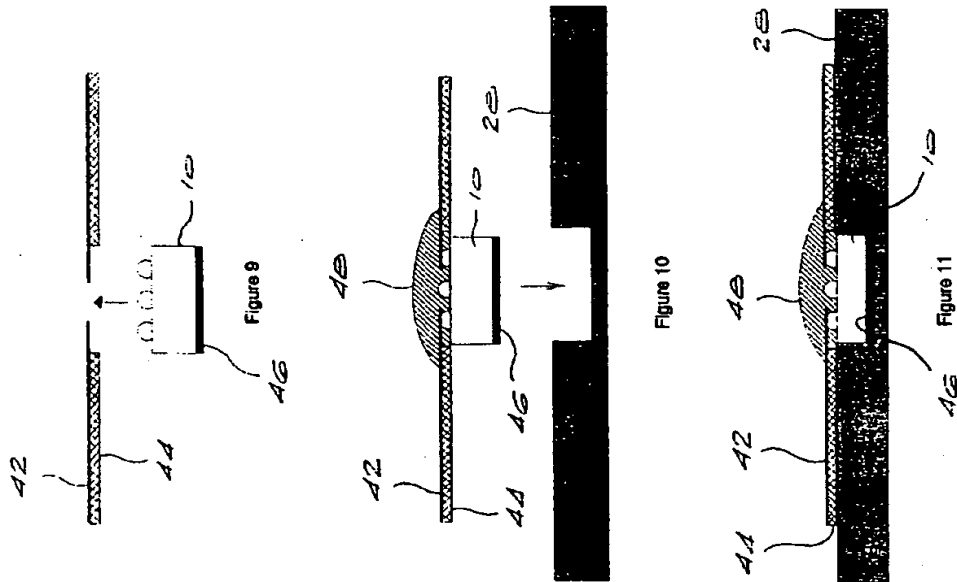


Figure 9

Figure 10

Figure 11

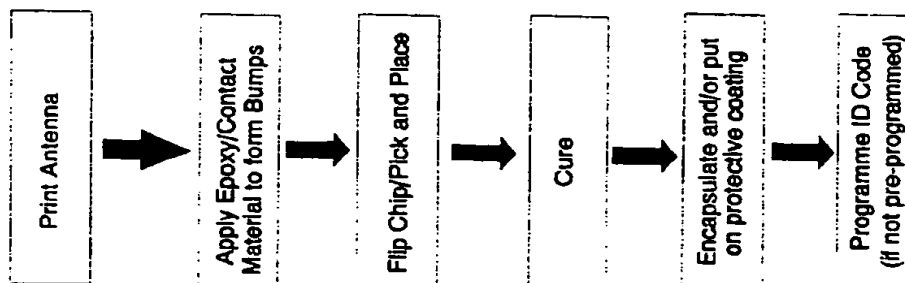


Figure 12

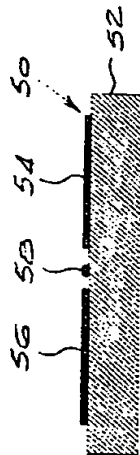


Figure 13

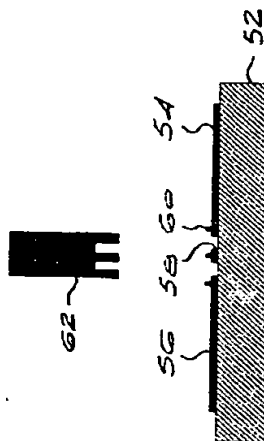


Figure 14

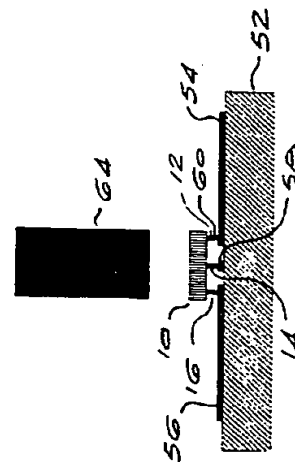


Figure 15

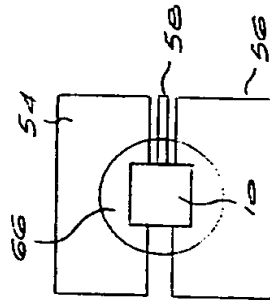


Figure 16a

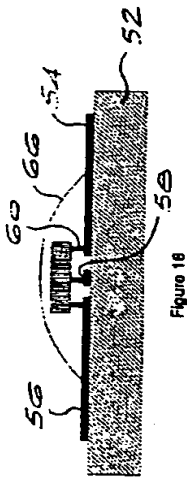


Figure 18

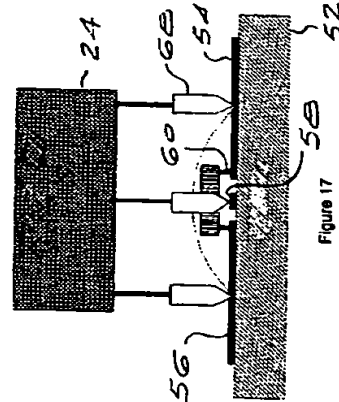
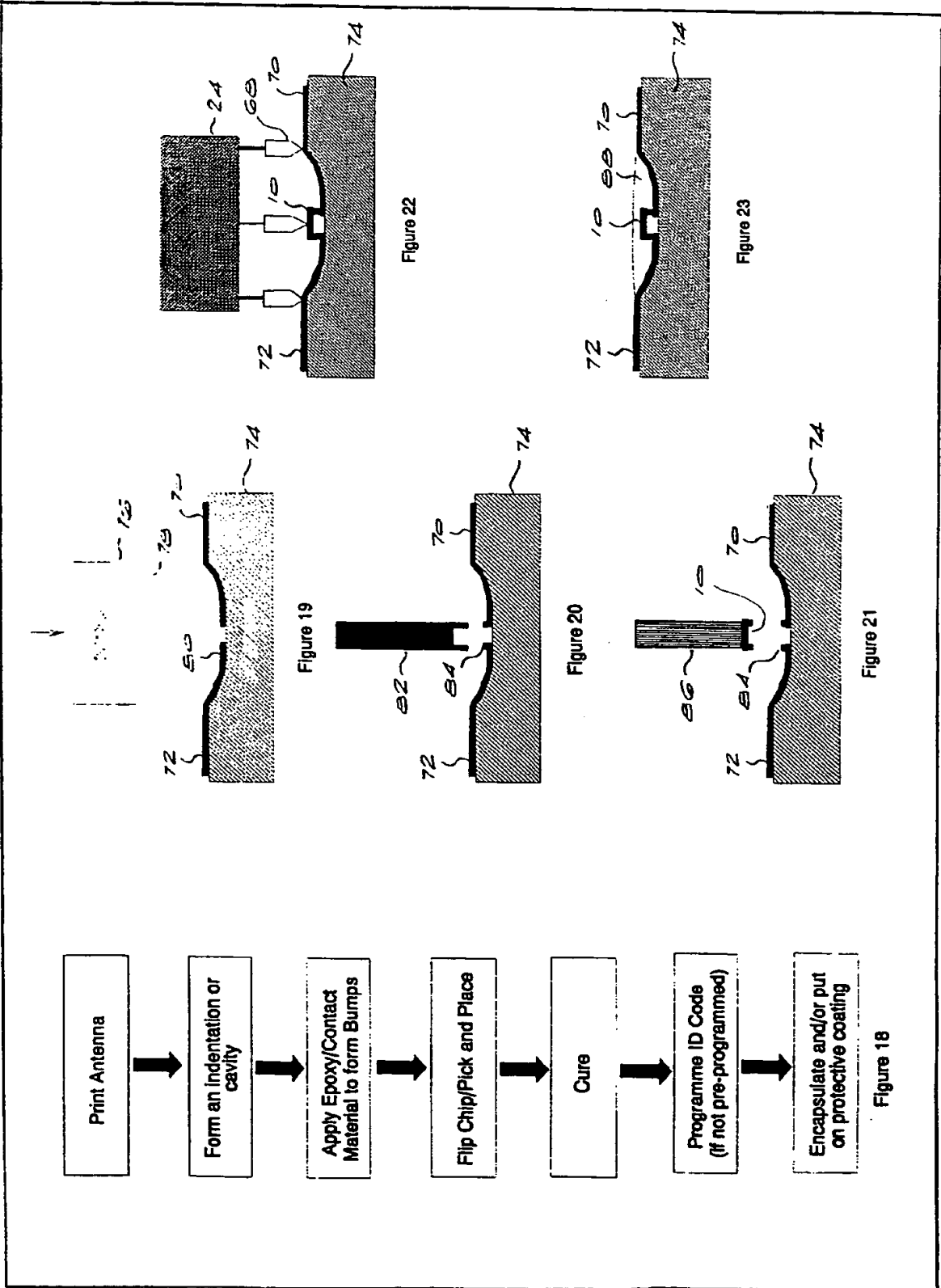
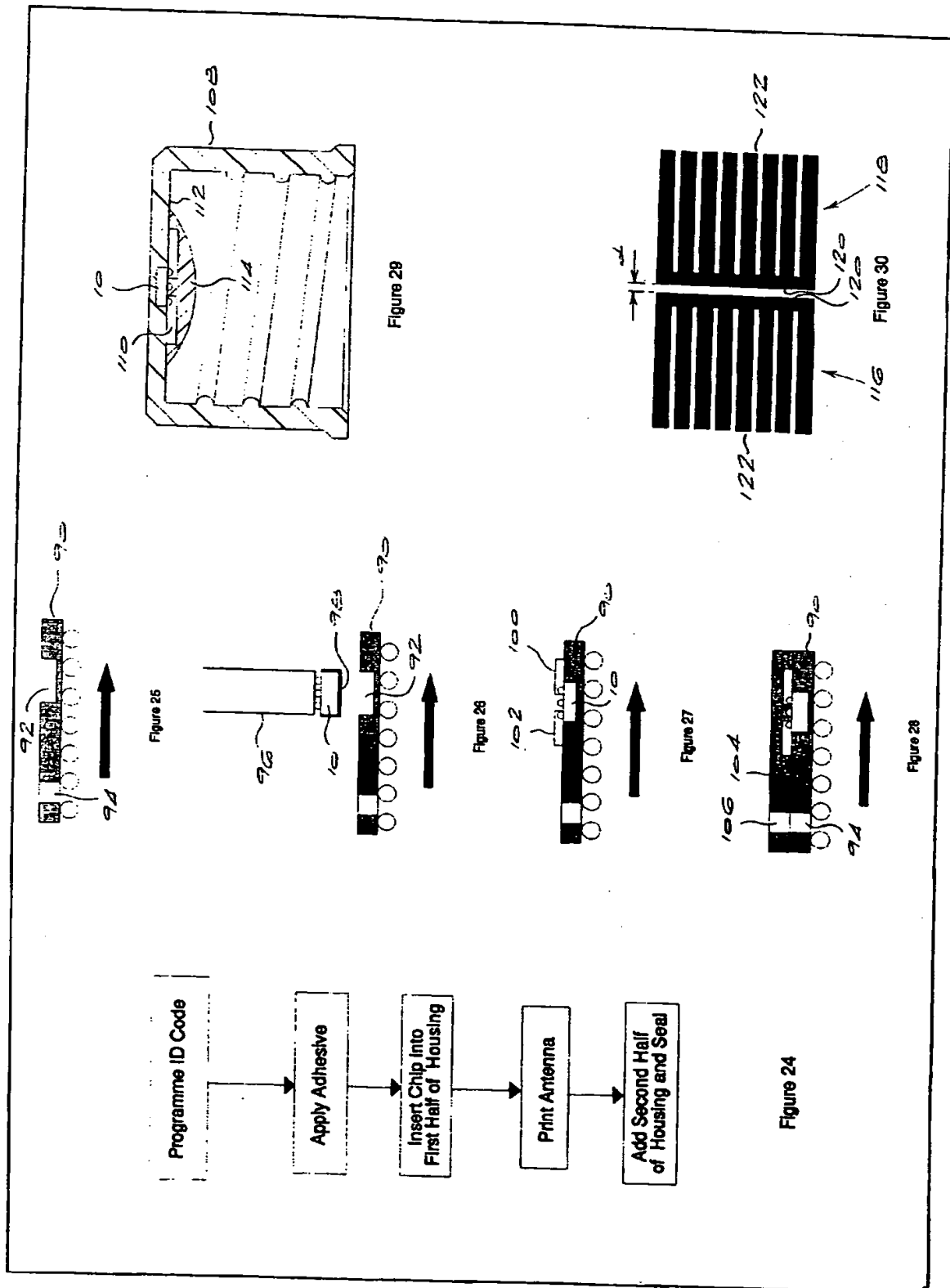
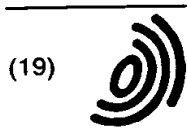


Figure 17









Europäisches Patentamt  
European Patent Office  
Office européen des brevets



(11) **EP 0 615 285 A3**

(12)

## EUROPEAN PATENT APPLICATION

(88) Date of publication A3:  
18.09.1996 Bulletin 1996/38

(51) Int Cl.<sup>6</sup>: **G06K 19/077, G06K 7/08,  
H01L 21/60**

(43) Date of publication A2:  
14.09.1994 Bulletin 1994/37

(21) Application number: **94301777.2**

(22) Date of filing: **11.03.1994**

(84) Designated Contracting States:  
**AT BE CH DE DK ES FR GB GR IE IT LI LU MC NL  
PT SE**

(30) Priority: **11.03.1993 ZA 931752**

(71) Applicant: **BRITISH TECHNOLOGY GROUP  
LIMITED**  
**London SE1 6BU (GB)**

(72) Inventors:  
• **Marsh, Michael J.C.**  
**Johannesburg, Transvaal (ZA)**

- **Gouws, Gideon J.**  
**Pretoria, Transvaal (ZA)**
- **Carson, Mark H.**  
**Pretoria, Transvaal (ZA)**
- **Marais, Mario A.**  
**Pretoria, Transvaal (ZA)**
- **Hodson, Trevor M.**  
**2153 Randburg, Transvaal (ZA)**

(74) Representative: **Funnell, Samantha Jane**  
**British Technology Group Ltd.**  
**Patents Department**  
**101 Newington Causeway**  
**London SE1 6BU (GB)**

### (54) **Attaching an electronic circuit to a substrate**

(57) Integrated circuits such as transponders (10) are attached to a substrate (28) such as cardboard packaging material by adhering an integrated circuit to the substrate, applying a patch antenna (18) to the substrate and the integrated circuit, and applying a sealing layer (40) over the integrated circuit and at least a portion of the antenna. In variations of the method, an in-

dentation (34) may be formed in the substrate into which the integrated circuit is placed. The patch antenna may be screen printed onto the substrate and the integrated circuit, using conductive ink, or may be a metallic foil which is applied to the substrate and the integrated circuit. Where the integrated circuit is a transponder, it can be programmed with an identification code, either before or after being attached to the substrate.

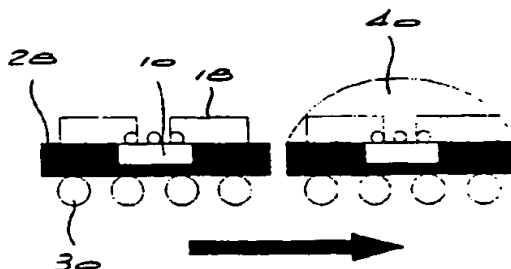


Figure 6

EP 0 615 285 A3



European Patent  
Office

# EUROPEAN SEARCH REPORT

Application Number  
EP 94 30 1777

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cls)
X	EP-A-0 079 047 (BROWN,BOVERI & CIE) * page 7, line 8 - line 17 *	1,32	G06K19/077 G06K7/08 H01L21/60
A	US-A-4 546 357 (LAUGHON T.C. ET AL.) * claim 1 *	2	
A	PATENT ABSTRACTS OF JAPAN vol. 17, no. 32 (E-1309), 21 January 1993 & JP-A-04 252523 (MITSUBISHI ELECTRIC CORP.), 8 September 1992, * abstract *	3	
A	MICROWAVE JOURNAL, vol. 19, no. 10, October 1976, USA, pages 47-49, XP002007867 GREISER,J.W.: "COPLANAR STRIPLINE ANTENNA" * column 2 * * figure 1B *	2	
A	DE INGENIEUR, vol. 84, no. 24, 16 June 1972, NL, pages 74-80, XP002007868 FLACHENECKER,G. ET AL.: "ACTIVE RECEIVING ANTENNAS" * page 76; figure 4 *	1	TECHNICAL FIELDS SEARCHED (Int.Cl.5)  G06K H01Q
A	PATENT ABSTRACTS OF JAPAN vol. 13, no. 34 (E-708), 25 January 1989 & JP-A-63 234601 (FUJITSU TEN LTD), 29 September 1988, * abstract *	7	
A	GB-A-2 100 669 (GAO GES FUR AUTOMATION UND ORGANISATION MBH) * abstract *	12,13	
The present search report has been drawn up for all claims			
Place of search <b>THE HAGUE</b>		Date of completion of the search <b>9 July 1996</b>	Examiner <b>Herskovic, M</b>
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons @ : member of the same patent family, corresponding document	
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document			

EPF FORM 1501 (01.92) (P04C01)



European Patent  
Office

**CLAIMS INCURRING FEES**

The present European patent application comprised at the time of filing more than ten claims.

- ☐ All claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for all claims.
- ☐ Only part of the claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims and for those claims for which claims fees have been paid.
- namely claims:
- ☐ No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims.

**LACK OF UNITY OF INVENTION**

The Search Division considers that the present European patent application does not comply with the requirement of unity of invention and relates to several inventions or groups of inventions,

namely:

SEE SHEET -B-

- ☒ All further search fees have been paid within the fixed time limit. The present European search report has been drawn up for all claims.
- ☐ Only part of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the inventions in respect of which search fees have been paid.
- namely claims:
- ☐ None of the further search fees has been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims.
- namely claims:



European Patent  
Office

# EUROPEAN SEARCH REPORT

Application Number  
EP 94 30 1777

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. CL.5)
A	GB-A-2 081 974 (GAO GESELLSCHAFT FUR AUTOMATION UND ORGANISATION MBH) * claim 1 *	14	
A	INTERNATIONAL ELECTRONIC MANUFACTURING TECHNOLOGY SYMPOSIUM, 26 - 28 April 1989, NARA, JAPAN, pages 101-104, XP000077902 WATAHIKI, S. ET AL.: "NEW PACKAGING TECHNOLOGY OF SUPER SMART CARD" * figure 1 *	16, 18	
A	DE-A-41 05 869 (SCHNEIDER, E.) * figure 2 *	17	
A	EP-A-0 412 893 (SCHLUMBERGER INDUSTRIES) * claims 1, 11 *	19-21, 27, 32	
A	EP-A-0 436 497 (TREND PLASTICS, INC.) * claims 5, 6 *	22	
A	US-A-4 004 133 (HANNAN, W. J. ET AL.) * claim 1 *	24	
A	GB-A-2 208 058 (ELECTROMATIC (PROPRIETARY) LTD) * claim 15 *	24	
A	US-A-3 755 803 (COLE, P. H. ET AL.) * column 2, line 34 - line 37 *	28	
A	FR-A-2 336 741 (KREFT, H. D.) * claim 11 *	29	
A	EP-A-0 268 089 (ETA S. A. FABRIQUES D'EBAUCHES) * column 4, line 21 - line 22 *	4	
		-/--	
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 9 July 1996	Examiner Herskovic, M
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document			

EPO FORM 150 (3.12.1994)



European Patent  
Office

EP 94301777-B-

# LACK OF UNITY OF INVENTION

The Search Division considers that the present European patent application does not comply with the requirement of unity of invention and relates to several inventions or groups of inventions, namely:

## Subject 1: Claims 1-3, 27-32

Method of attaching an electronic circuit or a packaging component comprising an integrated circuit to a substrate and applying a conductive element (layer of conductive ink comprising carbon or metallic particles) to define an antenna contacting at least 2 electrical terminals of the electronic circuit and the corresponding identification tag.

## Subject 2: Claims 1, 4-11, 27-32

Method of applying a metallic foil to contacting the electrical terminals of an integrated circuit or a packaging component comprising an integrated circuit and securing together on a substrate whether with a conductive adhesive or applying solder and the corresponding identification tag.

## Subject 3: Claims 1, 12-17, 27-32

Method of attaching an electronic circuit or a packaging component comprising an integrated circuit on a substrate by forming a cavity in the substrate made by punching, moulding, application of a heated tool, to receiving the electronic circuit; by heating the electronic circuit and pressing it in the substrate and the corresponding identification tag.

## Subject 4: Claims 1, 18-20, 26, 27-32

Packaging component comprising an integrated circuit and method of applying a protective coating or enclosure over the integrated circuit and over a portion of the antenna, said enclosure comprising a layer of a settable sealant or a label and the corresponding identification tag.

## Subject 5: Claims 1, 21-25, 27-32

Packaging component comprising an integrated circuit and method of attaching to a substrate the integrated circuit which is programmable with an identity code and the corresponding identification tag.



European Patent  
Office

# EUROPEAN SEARCH REPORT

Application Number  
EP 94 30 1777

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.5)
A	EP-A-0 132 183 (SLIGOS) * claim 1 *	15	
A	EP-A-0 246 744 (TOSHIBA K.K.) * claims 1,2 *	10	
A	EP-A-0 449 691 (SCHLUMBERGER INDUSTRIES) * claim 1 *	5	
			TECHNICAL FIELDS SEARCHED (Int.Cl.5)
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 9 July 1996	Examiner Herskovic, M
<p><b>CATEGORY OF CITED DOCUMENTS</b></p> <p>X : particularly relevant if taken alone  Y : particularly relevant if combined with another document of the same category  A : technological background  O : non-written disclosure  P : intermediate document</p> <p>T : theory or principle underlying the invention  E : earlier patent document, but published on, or after the filing date  D : document cited in the application  L : document cited for other reasons  &amp; : member of the same patent family, corresponding document</p>			

EPO FORM 150 01.12 (P/C01)